

IN THE SPECIFICATION:

Page 4, delete the third paragraph (starting at line 10) and replace it with the following new paragraph:

Fig. 1 is a block diagram illustrating an exemplary embodiment of a multi-processor system 10 built in an ASIC unit with which the invention may be utilized. The multi-processor system 10 is described in more detail in Applicant's co-pending U.S. patent application Serial No. XX/XXX,XXX 09/847,976, entitled "XXX MULTIPROCESSOR COMMUNICATION SYSTEM AND METHOD", which was filed on XX/XX/XXXX May 2, 2001 from which U.S. Patent No. 6938,253 issued on August 30, 2005, and which is incorporated herein by reference in its entirety. The system 10 is briefly described below.